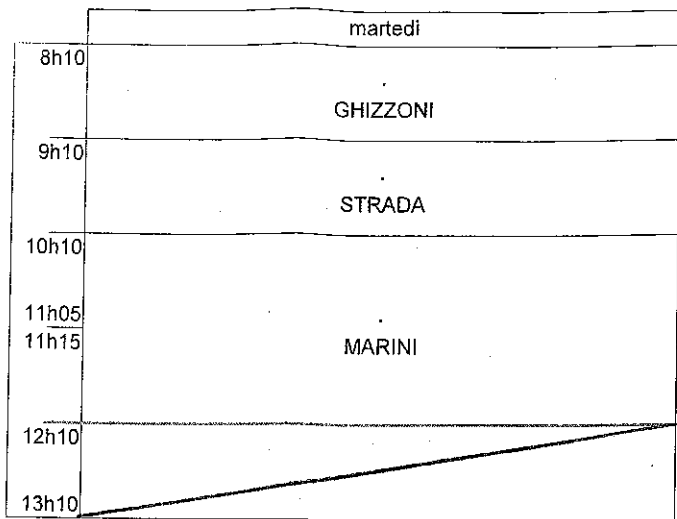
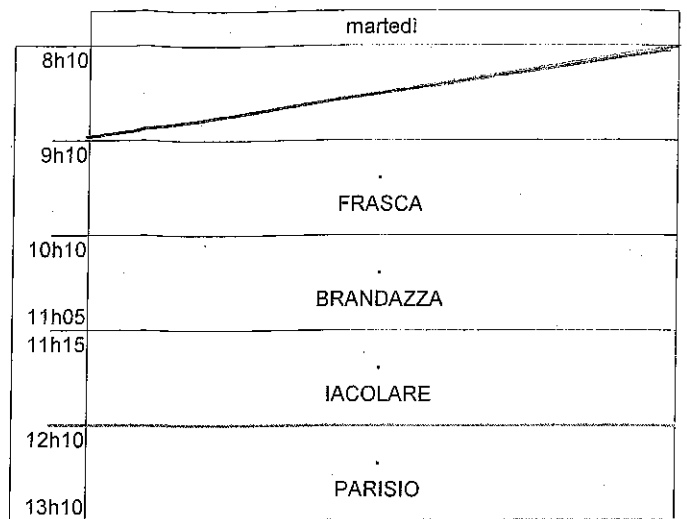


1A AT



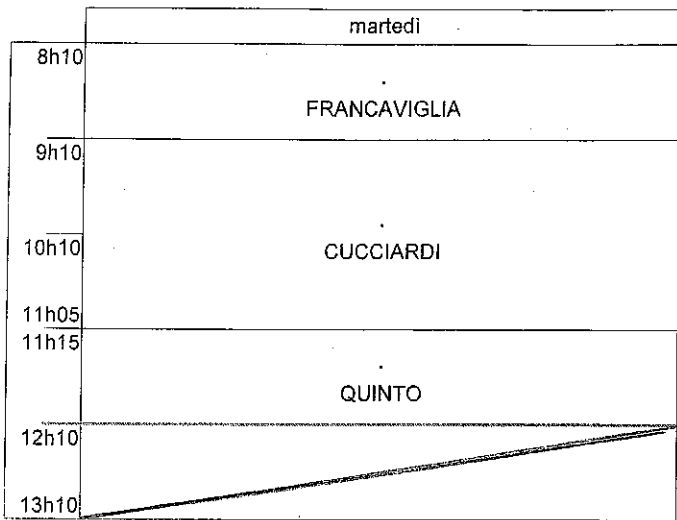
PROVVISORIO

1A SC



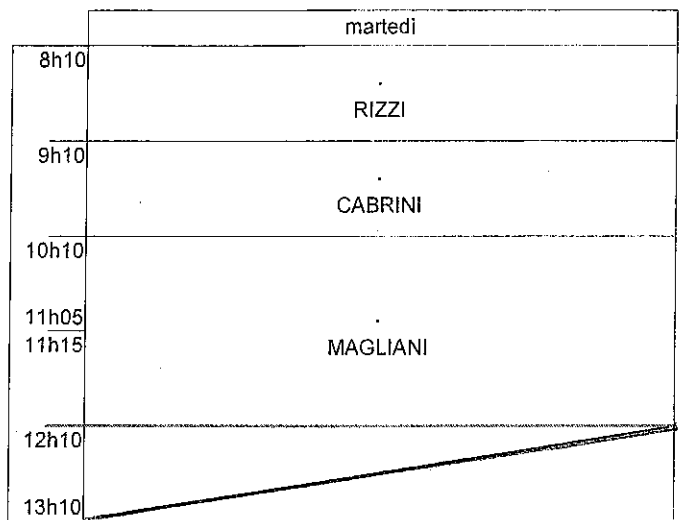
PROVVISORIO

1A SS



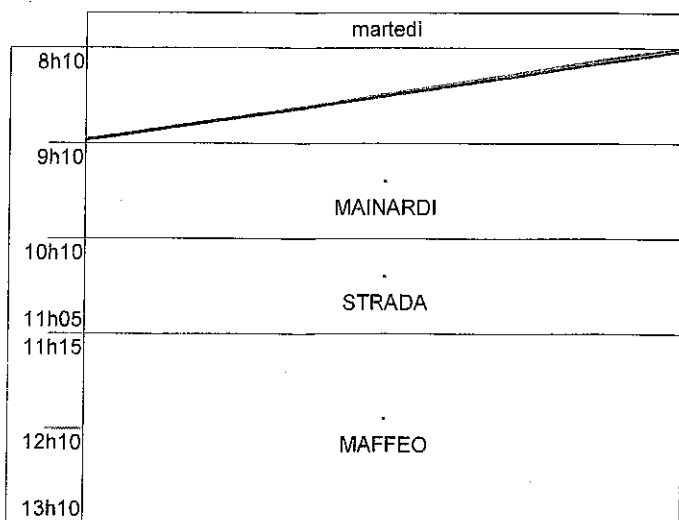
PROVVISORIO

1B AT



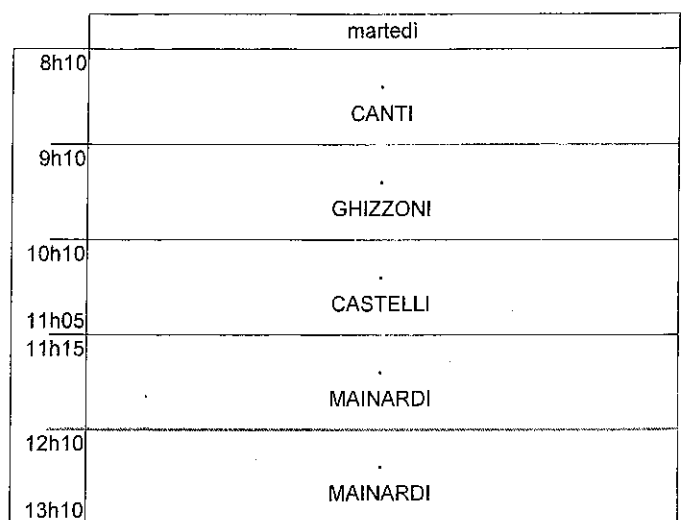
PROVVISORIO

1B SS



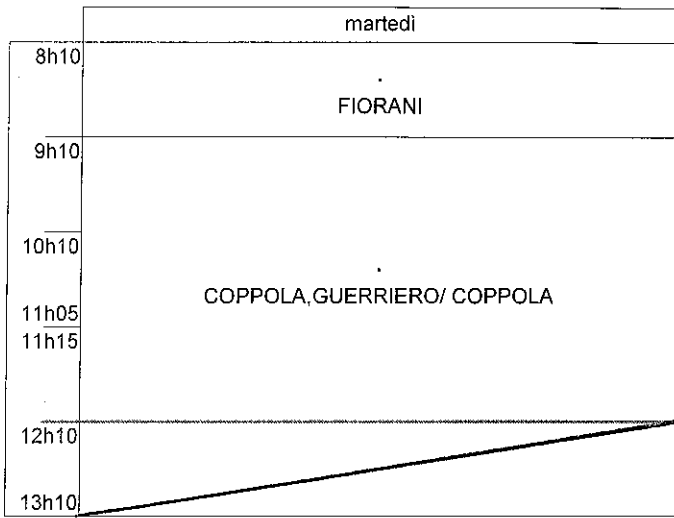
PROVVISORIO

1C SS



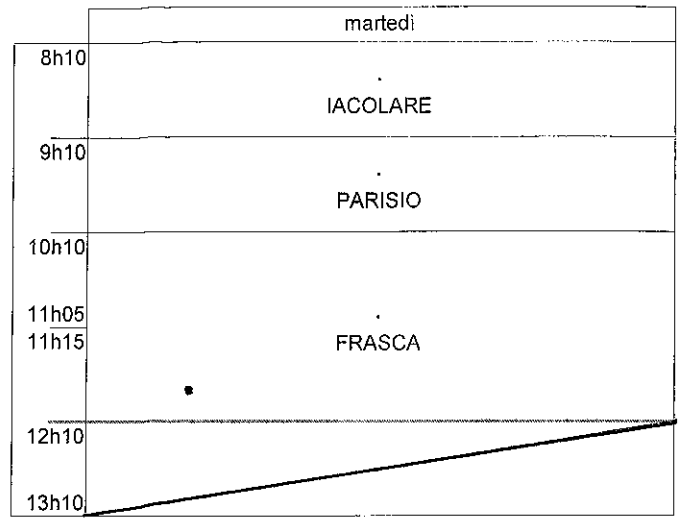
PROVVISORIO

2A AT



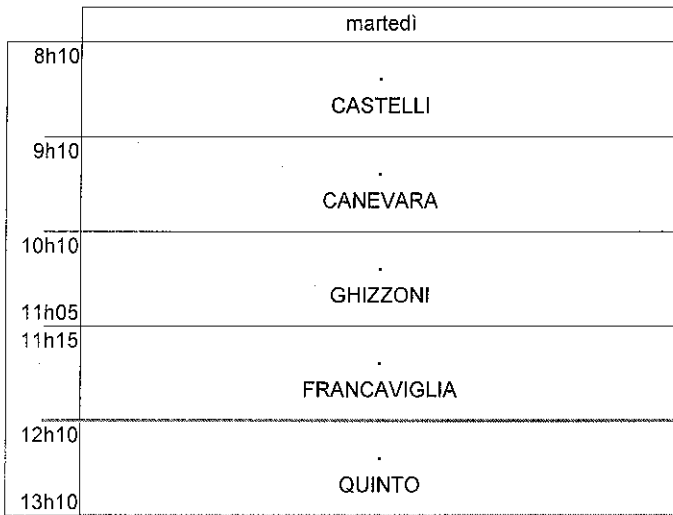
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2A SC



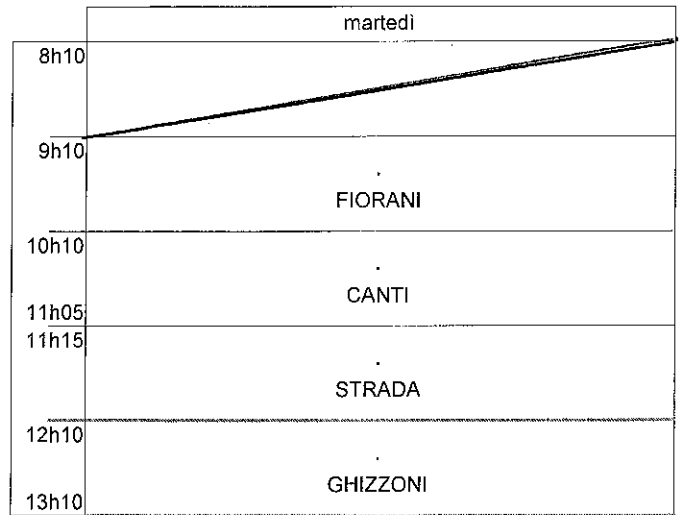
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2A SS



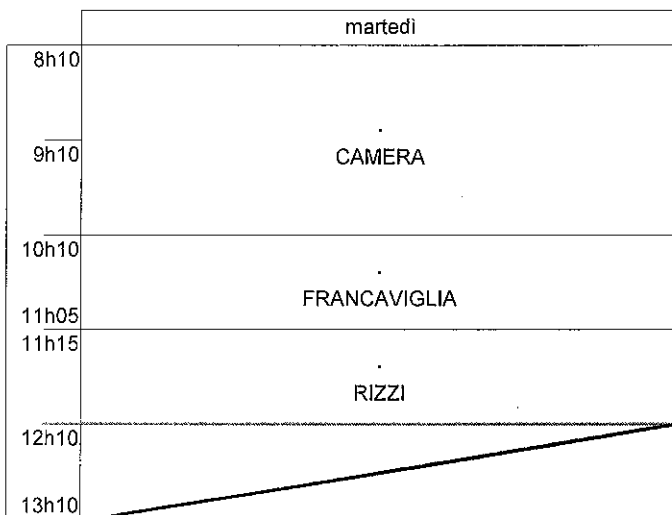
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2B AT



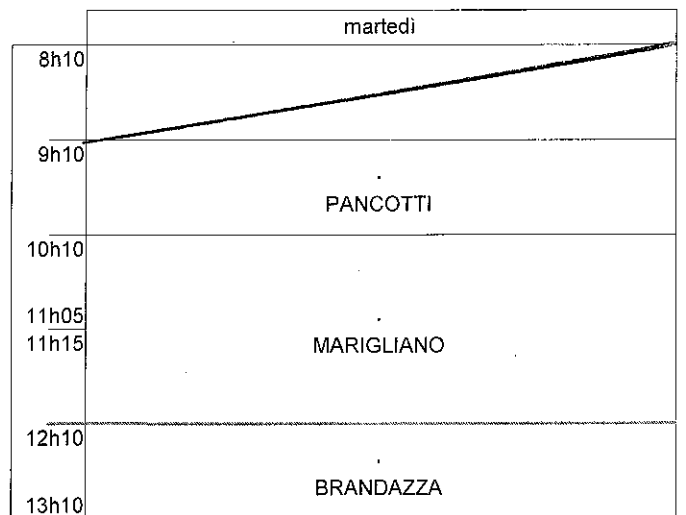
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2B SS



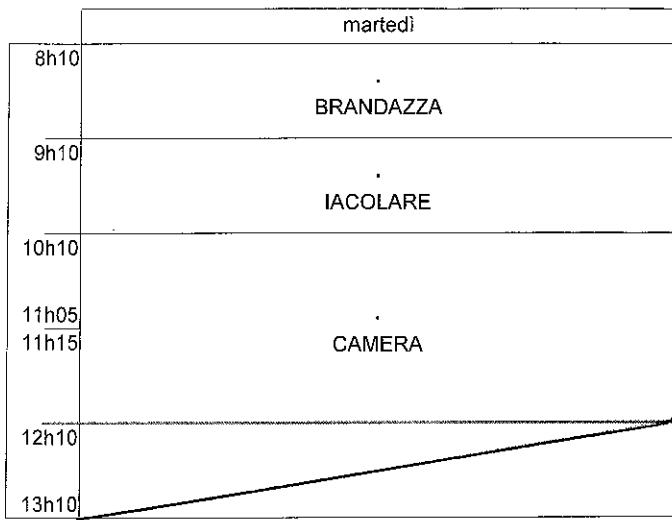
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3A AT



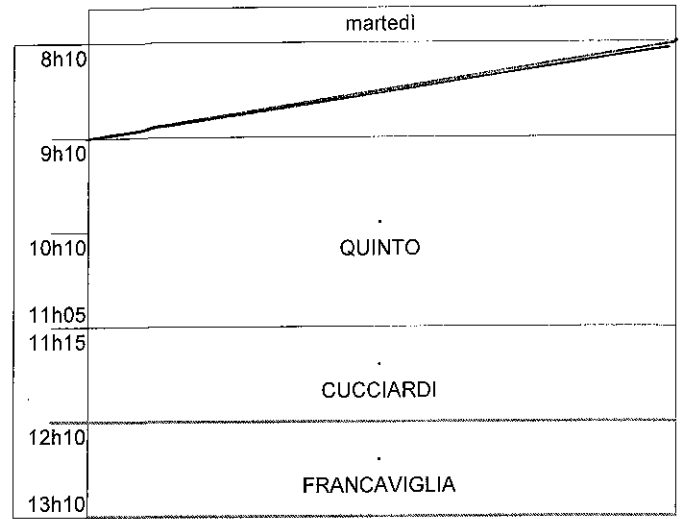
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3A SC



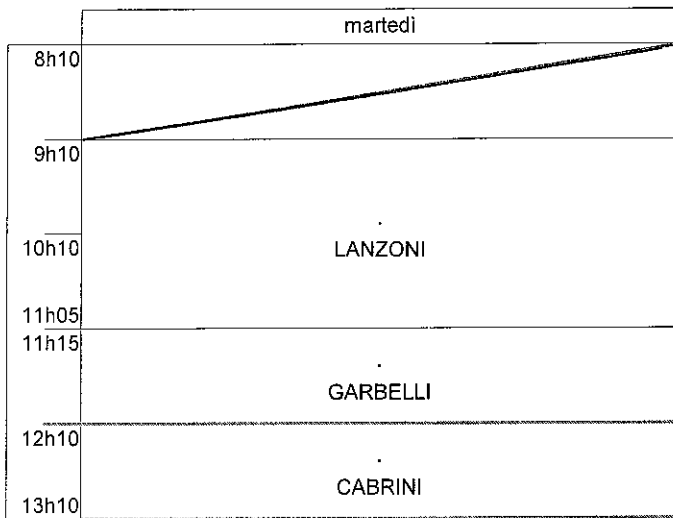
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3A SS



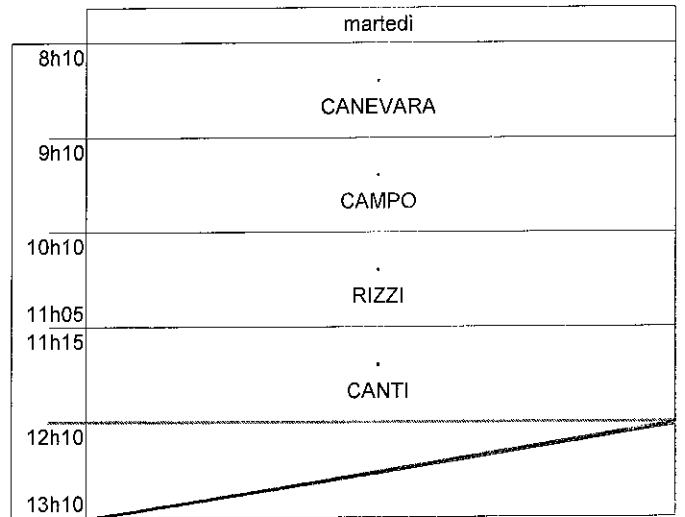
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3B AT



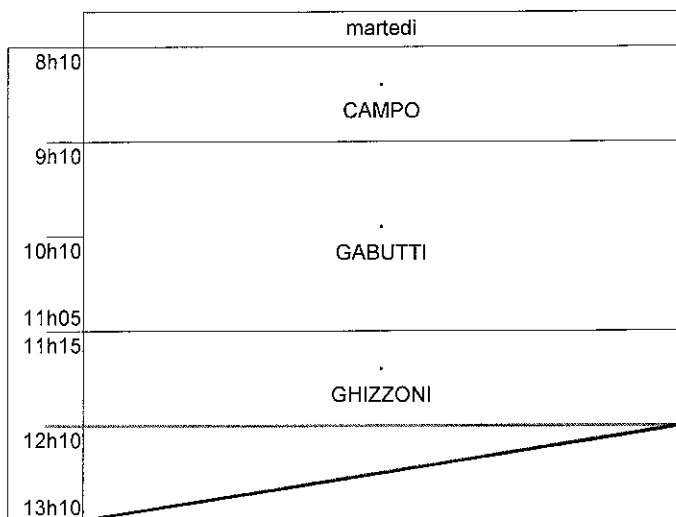
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3B SS



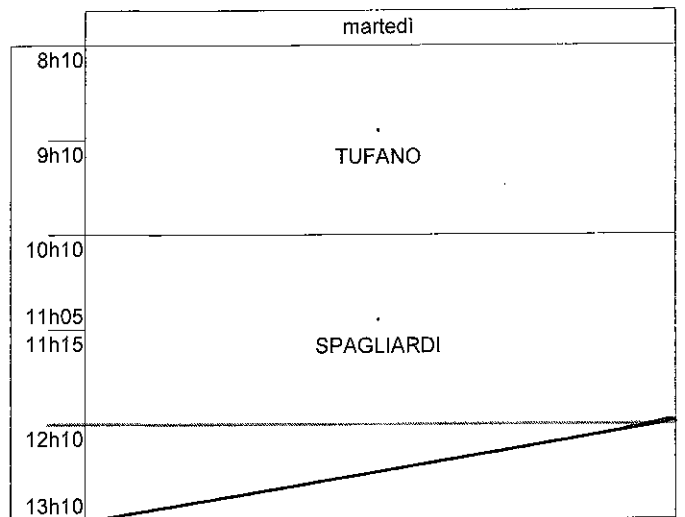
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3C SS



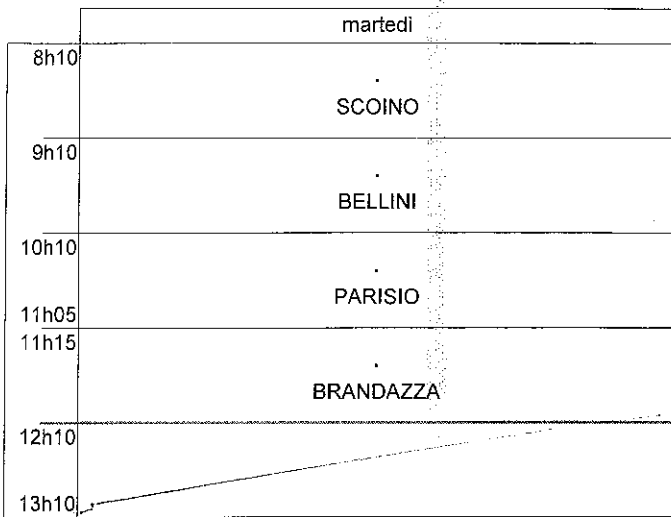
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4A AT



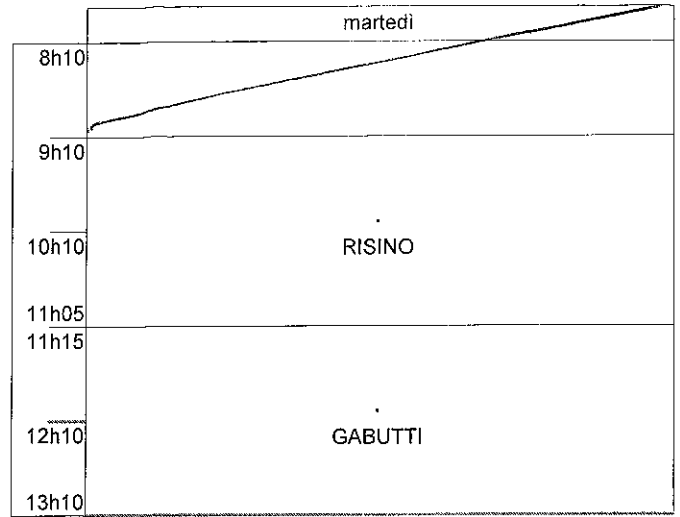
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4A SC



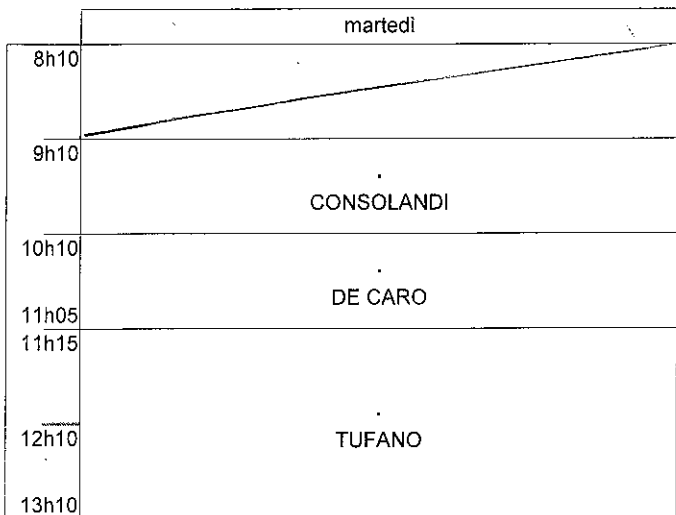
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4A SS



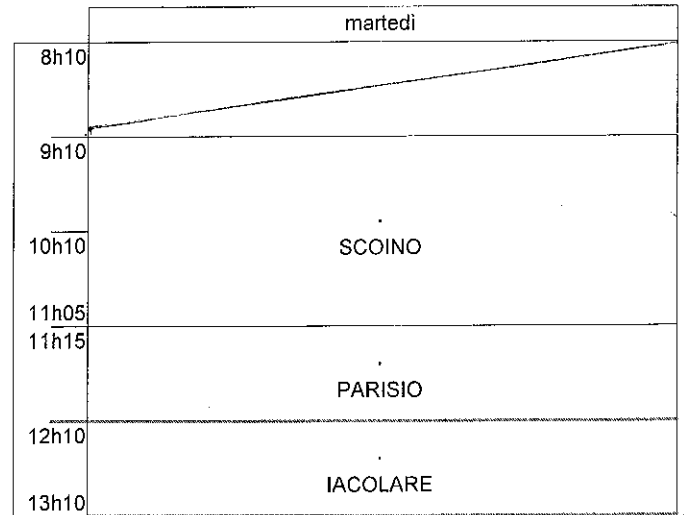
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4B AT



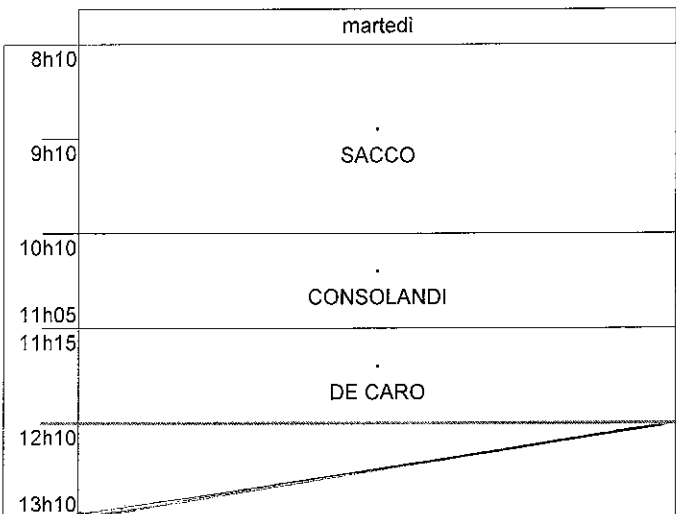
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4B SC



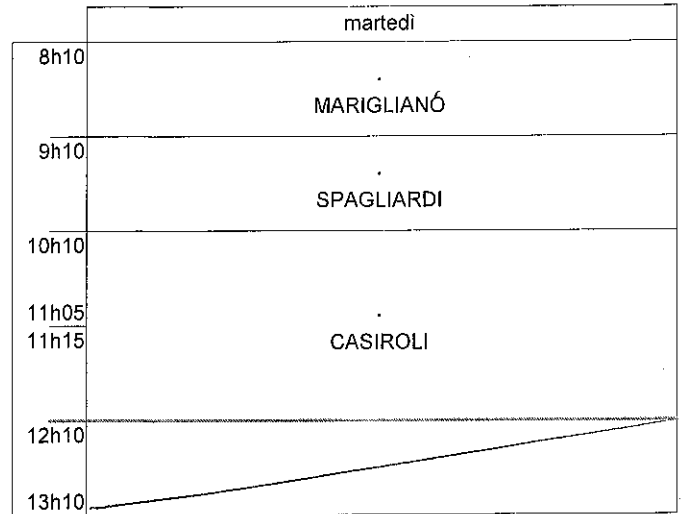
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4B SS



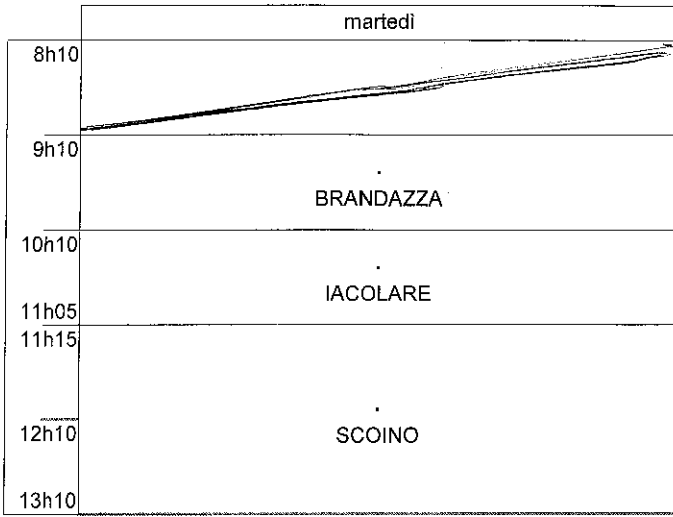
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5A AT



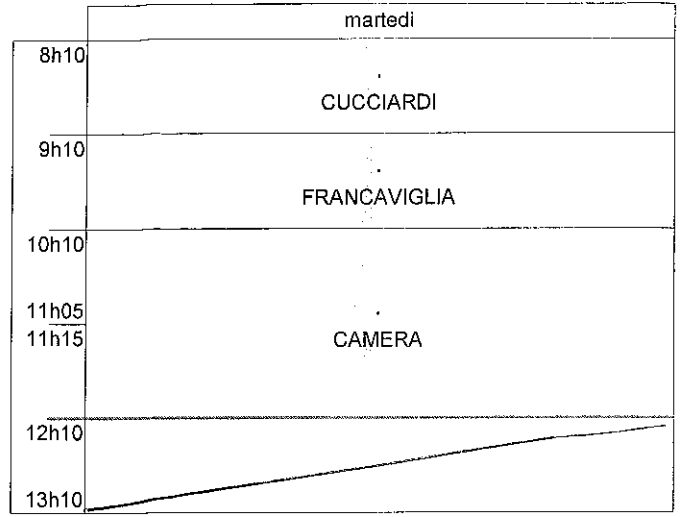
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5A SC



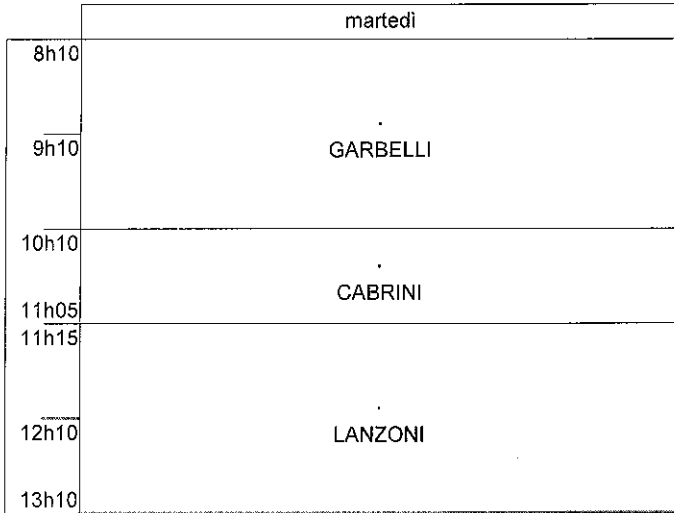
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5A SS



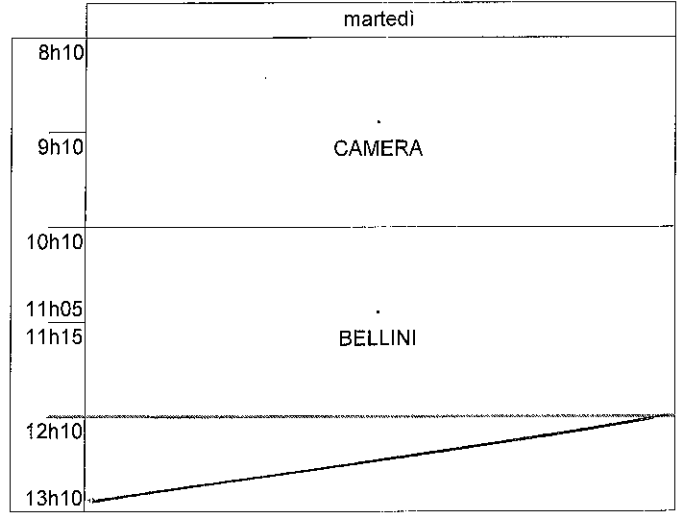
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5B AT



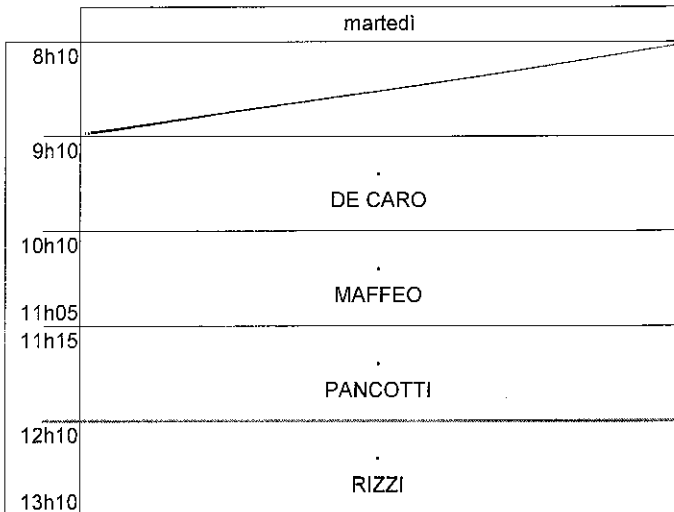
PROVVISORIO

5B SC



PROVVISORIO

5B SS



PROVVISORIO